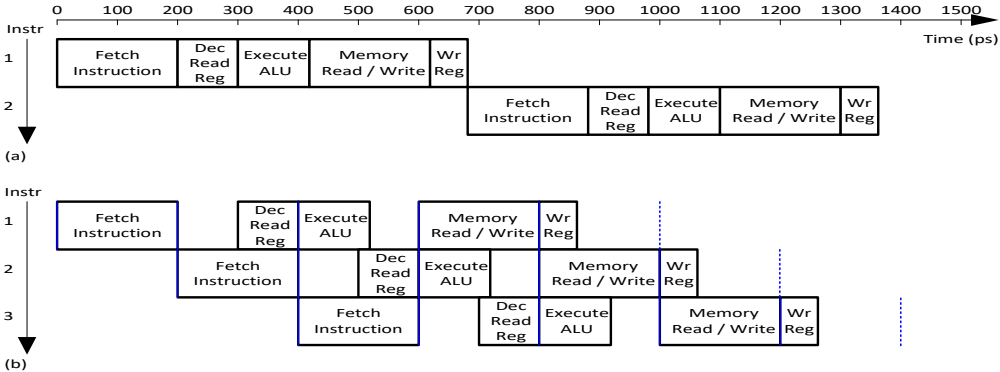
0
100
200
300
400
500
600
700
800
900
1000
1100
1200
1300
1400
1500
Time (ps)
Instr
1
2
Fetch Instruction
Dec Read Reg
Execute ALU
Memory Read / Write
Wr Reg
(a)
Instr
1
2
3
(b)

1500
me (ps)